SYMBIOSIS INSTITUTE OF TECHNOLOGY (SIT)
Constituent of SYMBIOSIS INTERNATIONAL (DEEMED UNIVERSITY)

M.TECH EMBEDDED SYSTEMS

OUR RANKINGS

INDIA TODAY 2020

1ST
Among Emerging Private
Colleges of the Century

2ND
Among Private Engineering
Colleges in West Zone

OUTLOOK 2019

1ST
Among all Private Engg.
Colleges in Pune

3RD
Among Private Colleges
in Maharashtra

16TH
Among Private Engineering
Colleges in West Zone

DEPARTMENT OF ELECTRONICS & TELECOMMUNICATION ENGINEERING

This programme is a part of the Electronics and Telecommunication Engineering department of Symbiosis Institute of Technology (SIT). It aims to develop an understanding of software, hardware, and tools used for designing embedded systems.

The M. Tech. (Embedded Systems) at SIT, Pune is a rigorous skill-based and practice-oriented program designed with a blend of Flexi courses, experiential learning, projects, and assignments. The dissertation work in collaboration with industry will provide students a real-time experience and produce industry-ready professionals. The highly experienced faculty at SIT and experts from the industry will offer a high-end teaching-learning experience. The state-of-the-art hardware and software tools present in the laboratories provide a unique understanding of theoretical concepts and hands-on practice.

Students can experience working with microcontrollers, do sensor analysis and calibration, do communication setup, perform modeling and simulations, setup IoT infrastructure, and many more. The lab environment is conducive to the student's overall learning experience.

At SIT, students practice various lab experiments as part of their curriculum and work on projects independently and in collaboration with research faculty and industry mentors.

The total 80 credits of the course structure are divided into two parts, coursework and dissertation work. The coursework comprises various compulsory and elective courses providing students the choice to upskill the knowledge in interested field. A student can complete the dissertation work at a research organization or can avail an opportunity to get industrial experience by doing the internship.

The curriculum developed in consultation with industry personnel will surely help students upskill themselves and excel in their prospective careers.

HIGHLIGHTS

01 Flexi Credit Courses

02 One Year Industry Internship

03 Industry Collaborations

04 Global Immersion Program

05 Special Diploma in Business Management

06 Mentor-Mentee System and Remedial Teaching

07 Academic Curricula Benchmarked with International Universities

08 Curriculum Developed in Consultation with KPIT

09 Industrial Visits, Workshops, Technical Events, Guest Lectures

10 Learn Foreign Languages

11 Tinker Laboratory

12 Best in Class Infrastructural Facilities

13 Service Learning Activities

14 Creative Thinking, Critical Thinking, Design Thinking, Entrepreneurship Courses

INDUSTRY ALIGNED ONE YEAR INTERNSHIP

- Over 200 Companies on board from 2013 to till date.
- Over 700+ students successfully completed their internship across various disciplines of Electronics and Telecommunication Engineering.
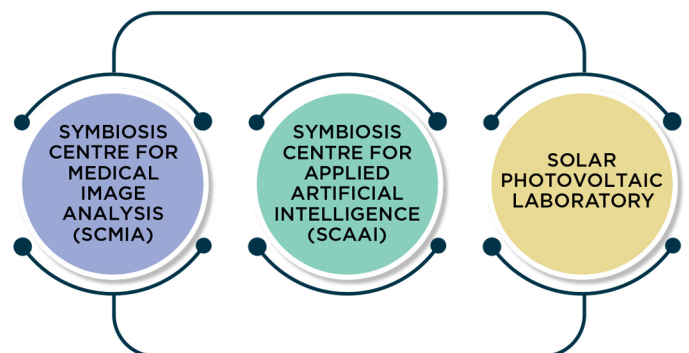- 5 students completed their internship internationally.

INTERNATIONALIZATION

Our students participated in semester exchange programs of the following partner Universities under the Global Immersion Program:

- **Indiana University–Purdue University Indianapolis (IUPU), USA**
- **Ontario-Maharashtra-Goa Consortium (OMG) - Western University, Ontario**
- **Nanyang Technological University, Singapore**
- **Ingolstadt University, Germany**
- **Leibnitz University, Germany**
- **Telecom Ecole de Management, Paris**

CURRICULUM HIGHLIGHTS

- ARM Cortex Architecture and Programming
- Operating Systems and RTOS
- Modeling and Simulation
- Embedded Systems for Machine Learning Applications
- Cyber Security
- Internet of Things (IoT) and Applications
- Computer Vision and Healthcare

RESEARCH CENTERS/LABORATORY

SYMBIOSIS CENTRE FOR MEDICAL IMAGE ANALYSIS (SCMIA)

SYMBIOSIS CENTRE FOR APPLIED ARTIFICIAL INTELLIGENCE (SCAAI)

SOLAR PHOTOVOLTAIC LABORATORY

The centers and labs focuses on real time applications.

MOU WITH PHILIPS, KPIT, SPARK MINDA FOR COURSE CURRICULUM DEVELOPMENT, COLLABORATIVE PROJECTS, STUDENTS' INTERNSHIPS AND POSSIBLE PLACEMENTS

PLACEMENTS @ SIT E&TC

TOP RECRUITERS

EMBEDDED SYSTEMS

INTAKE: 18

Eligibility for M. Tech Embedded Systems: Engineering Graduate (B.E./B.Tech.) in relevant disciplines with minimum of four years duration from any recognized University/Institution with a minimum of 50% marks or equivalent grade (45% Marks or equivalent grade for Scheduled Caste/Scheduled Tribes)

• GATE qualified students can apply based on valid GATE Score.

• Non-GATE students will be required to appear for an entrance examination and based on the score, admission will be offered.

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